

Figure 2

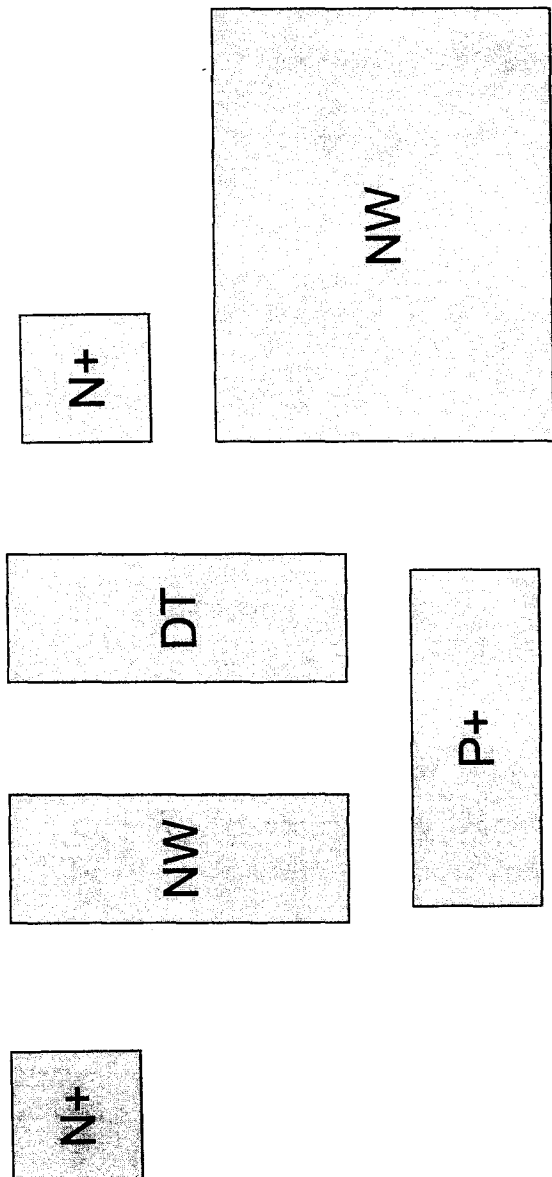


Figure 3

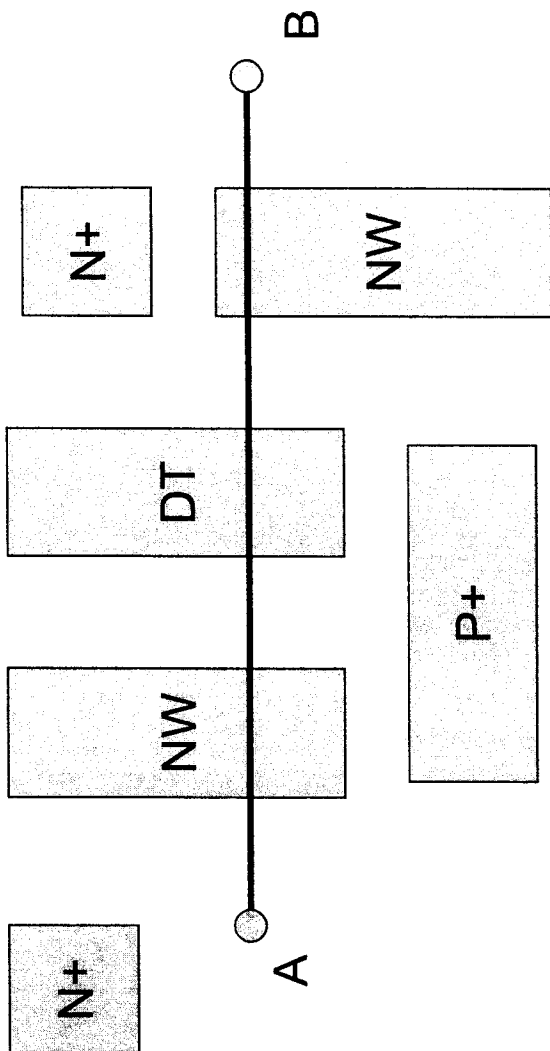


Figure 4

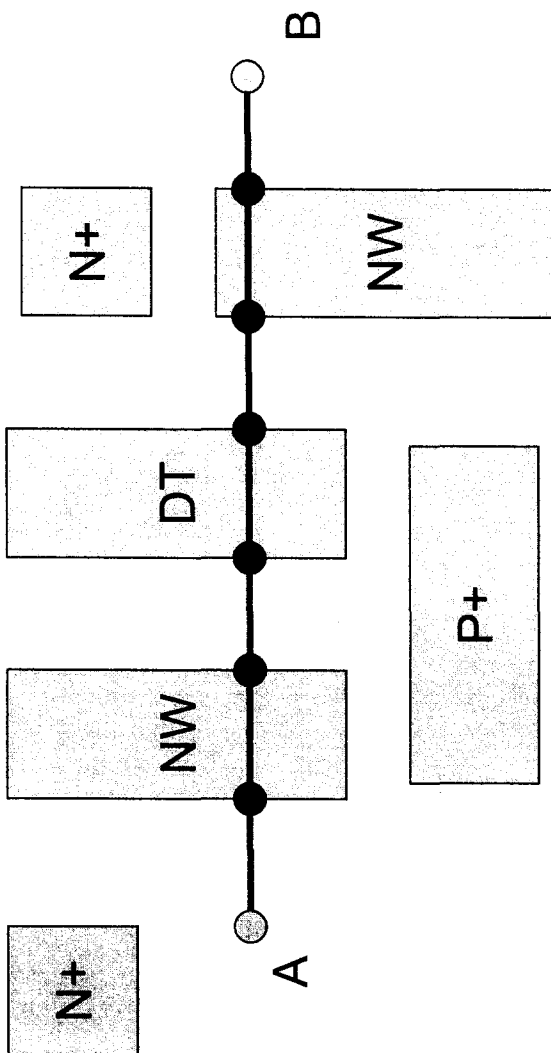


Figure 5a

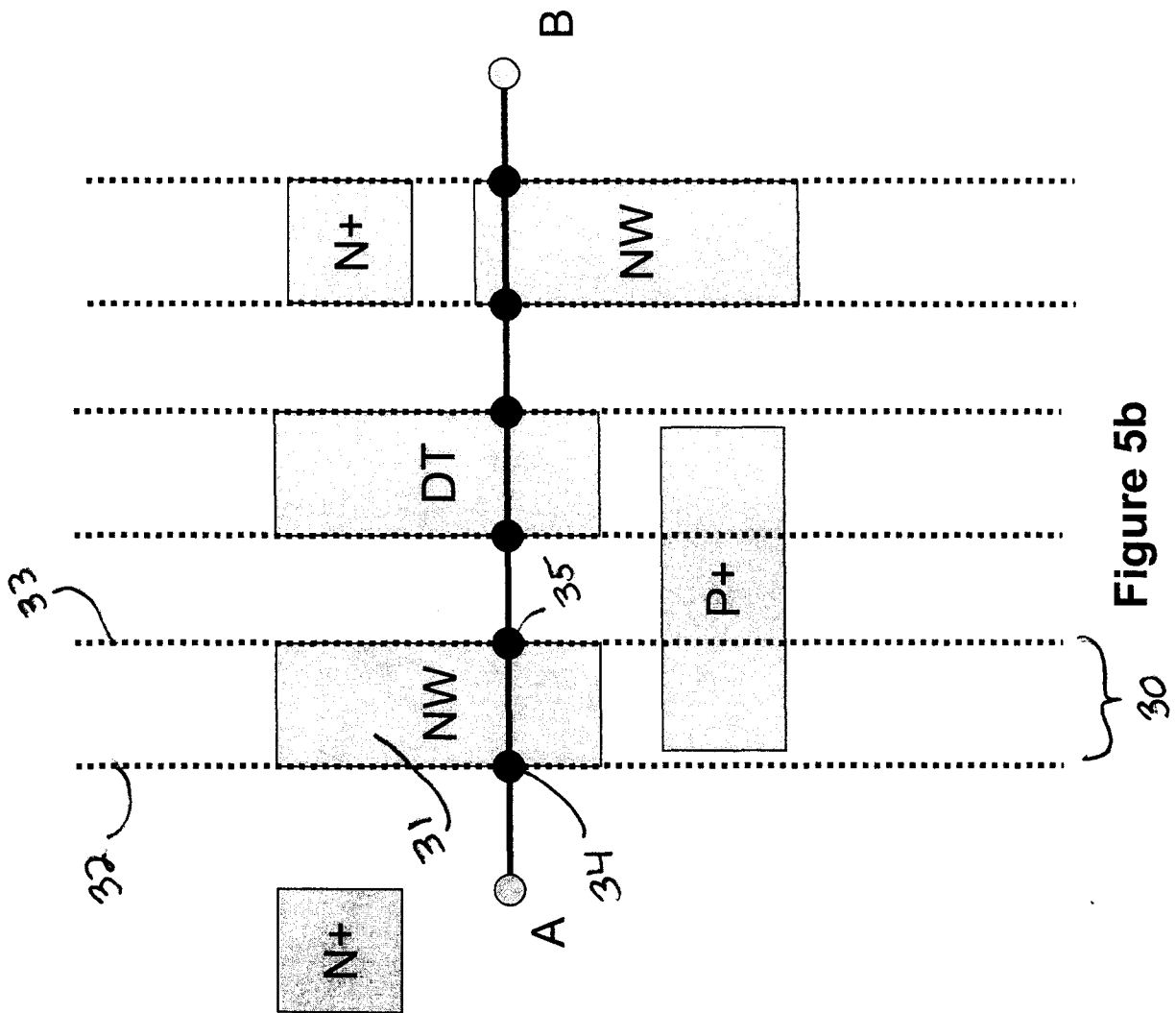
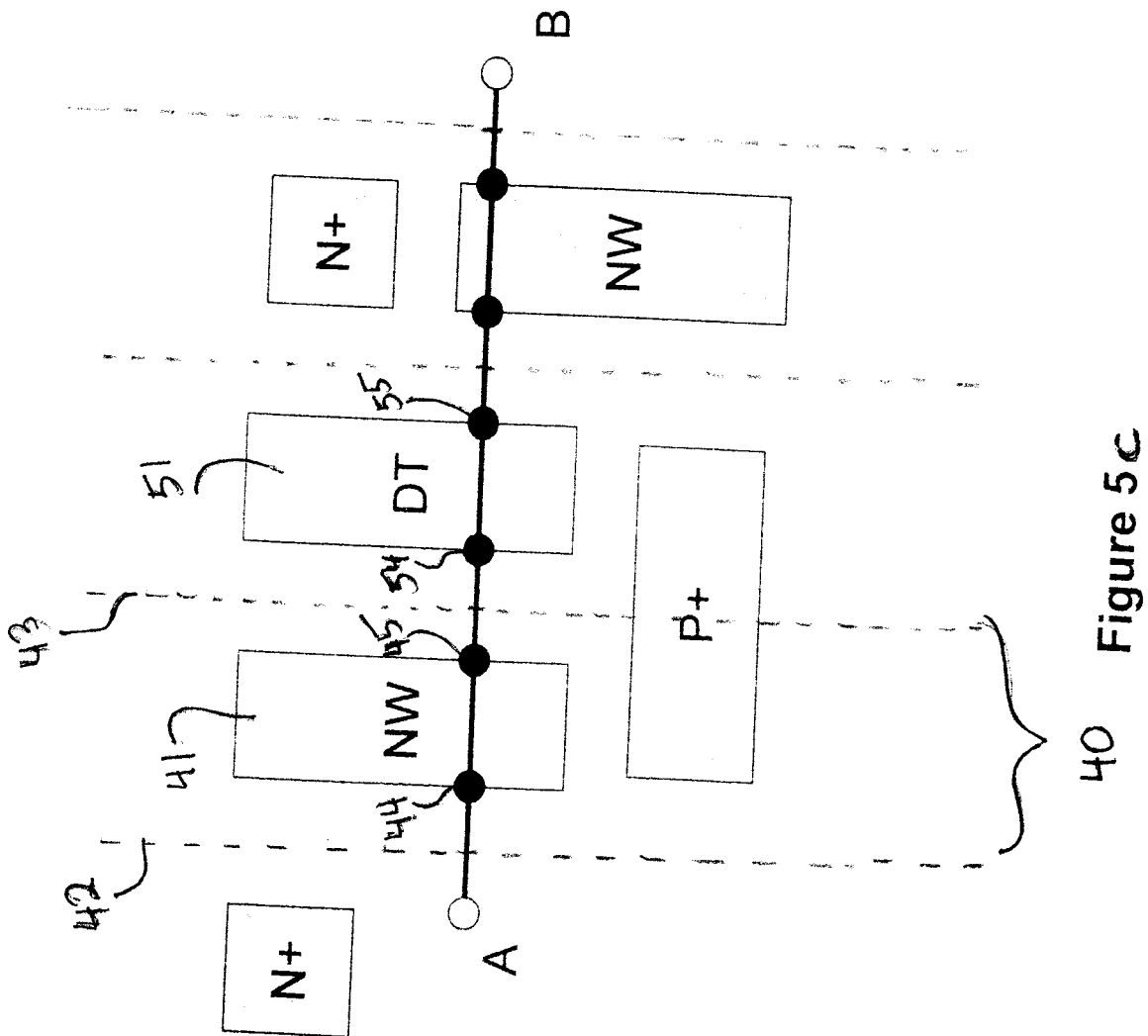


Figure 5b



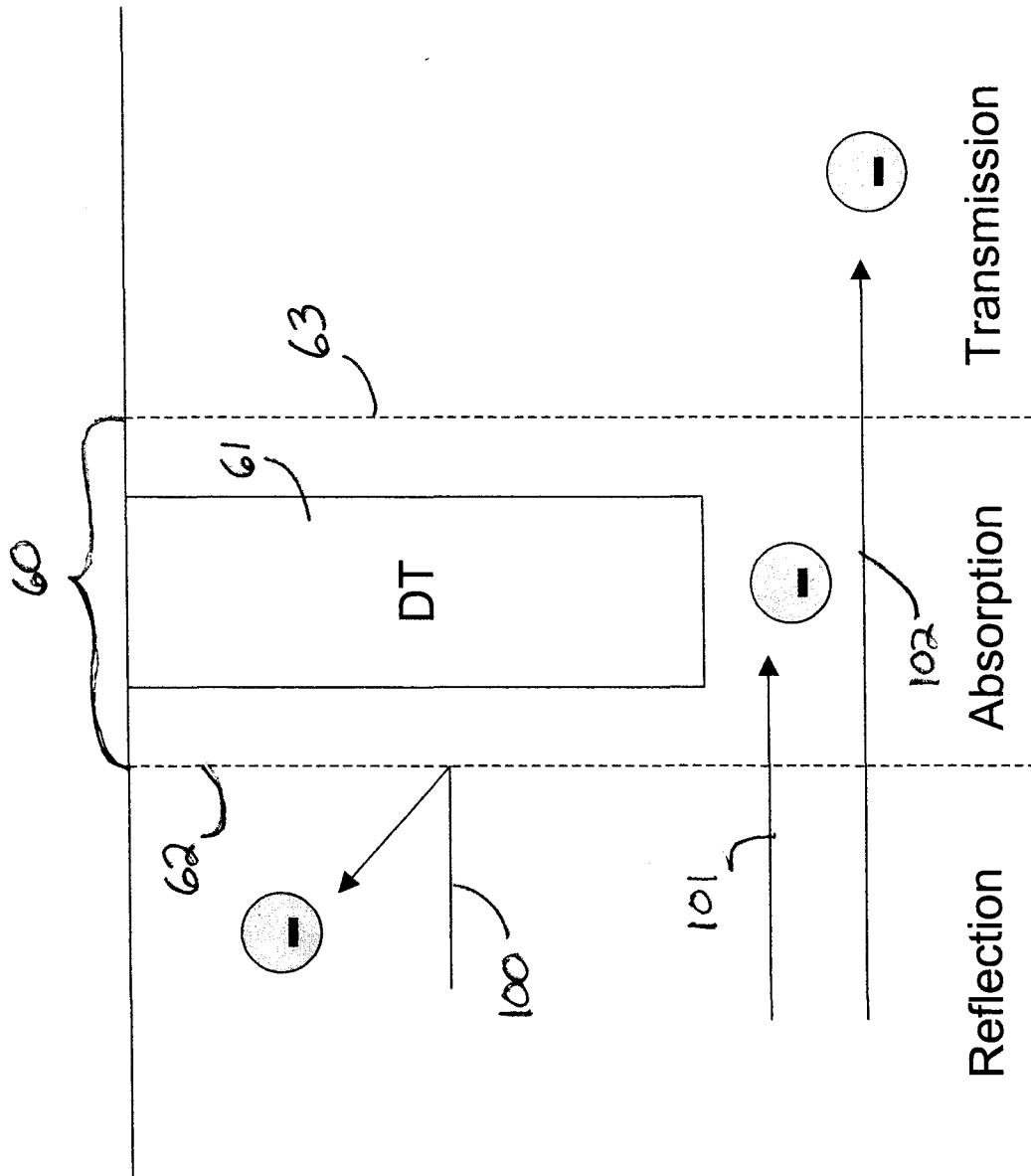


Figure 6a

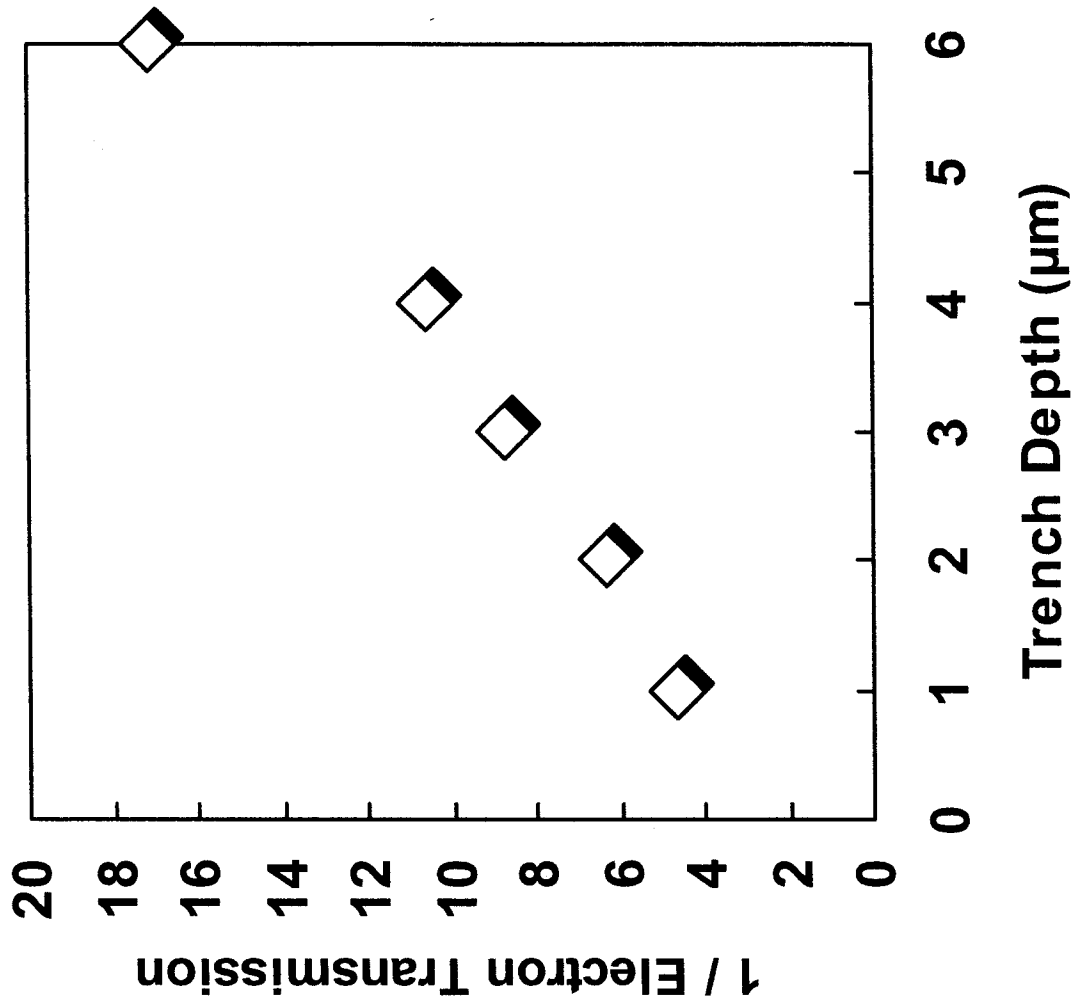


Figure 6b



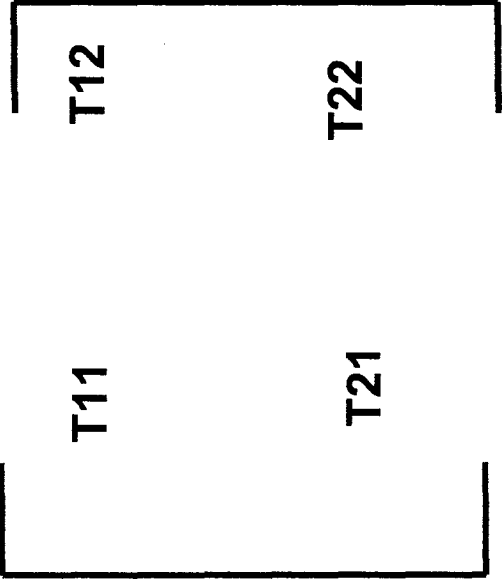
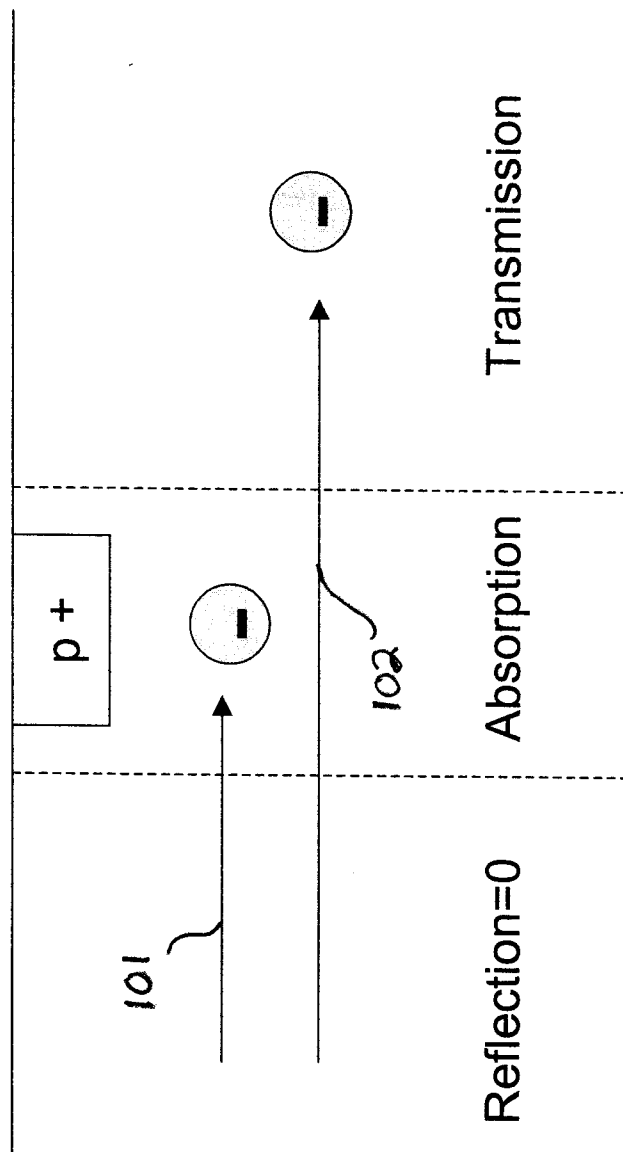


Figure 6c



**Figure 7**

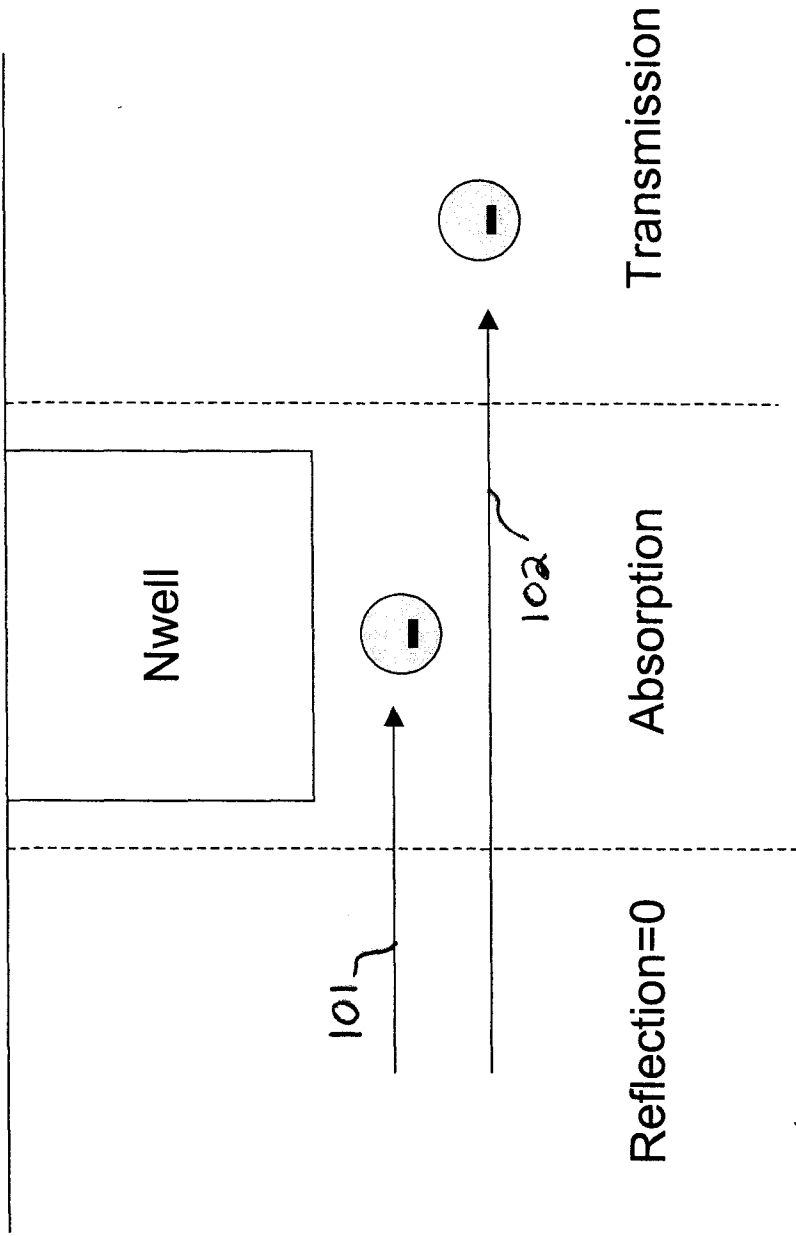


Figure 8

UNIT - CELL

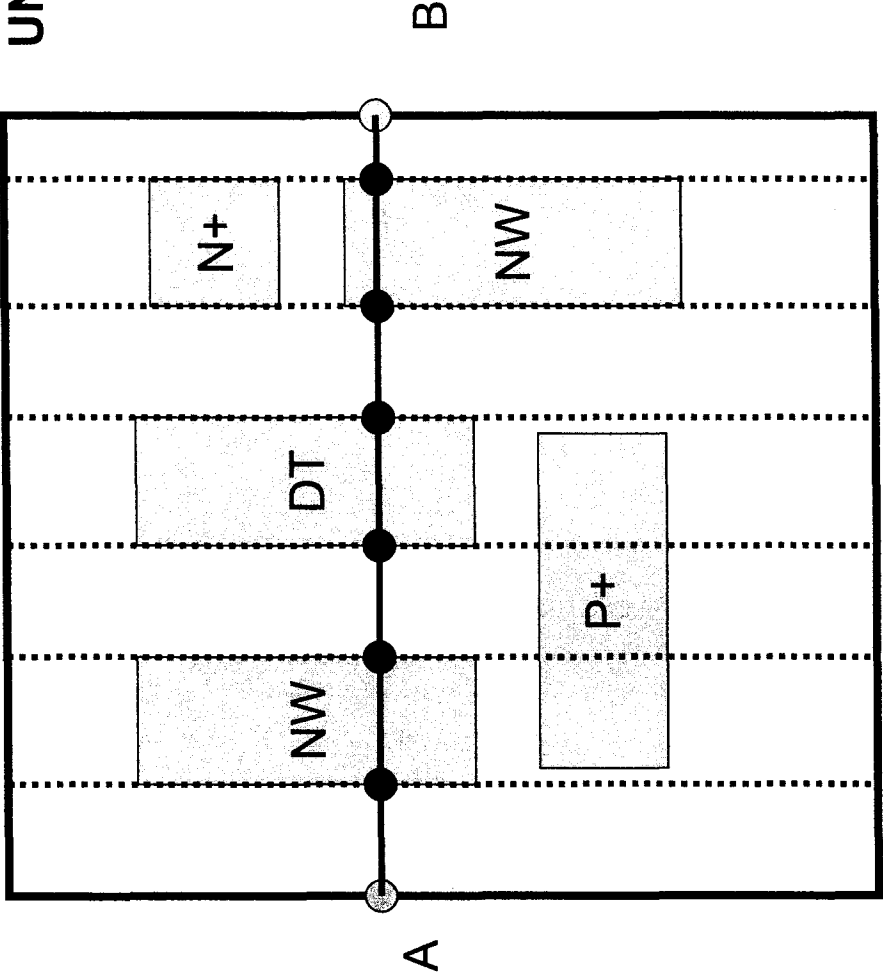


Figure 9

ARRAY

UNIT CELL

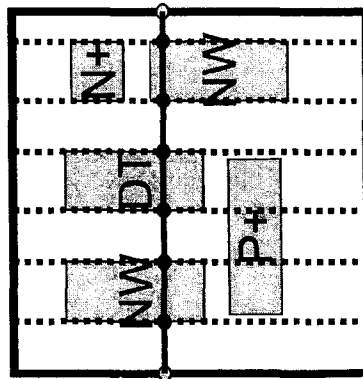
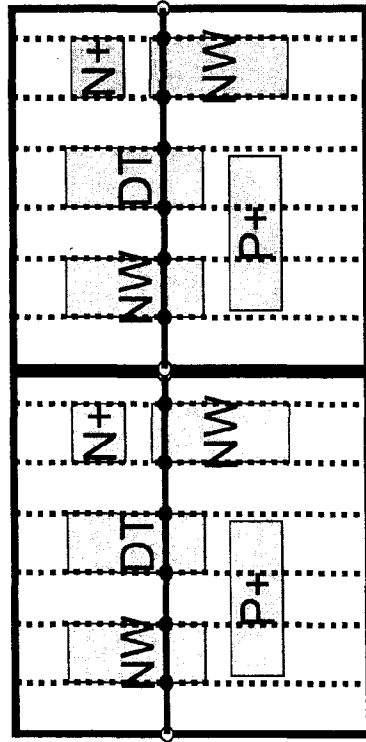


Figure 10

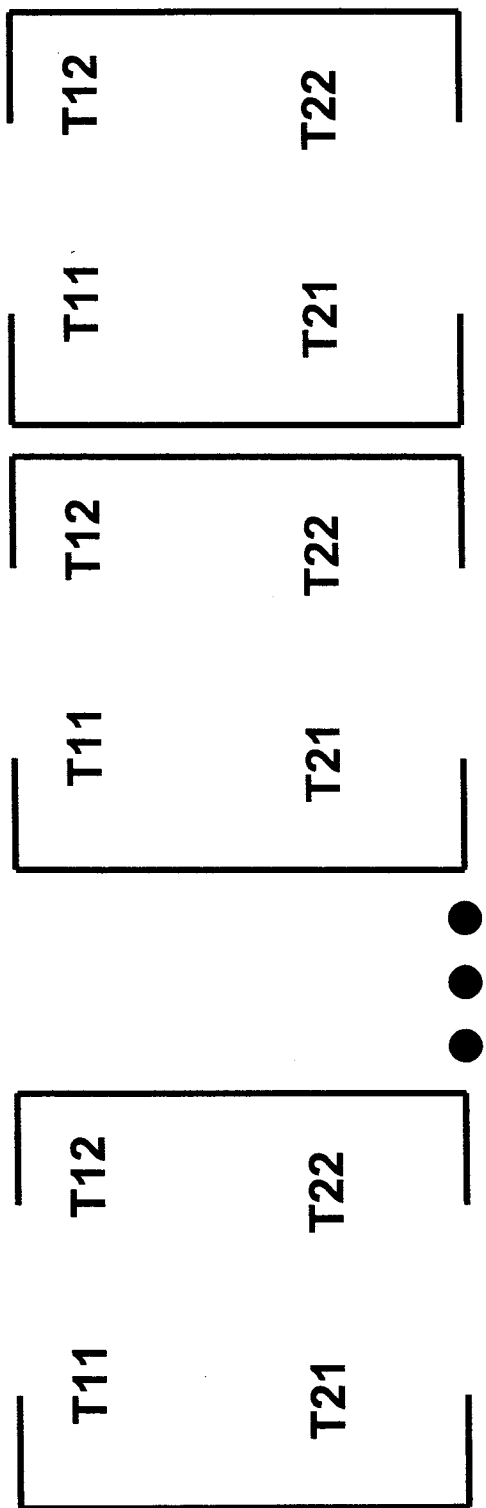


Figure 11 N- Unit Cells



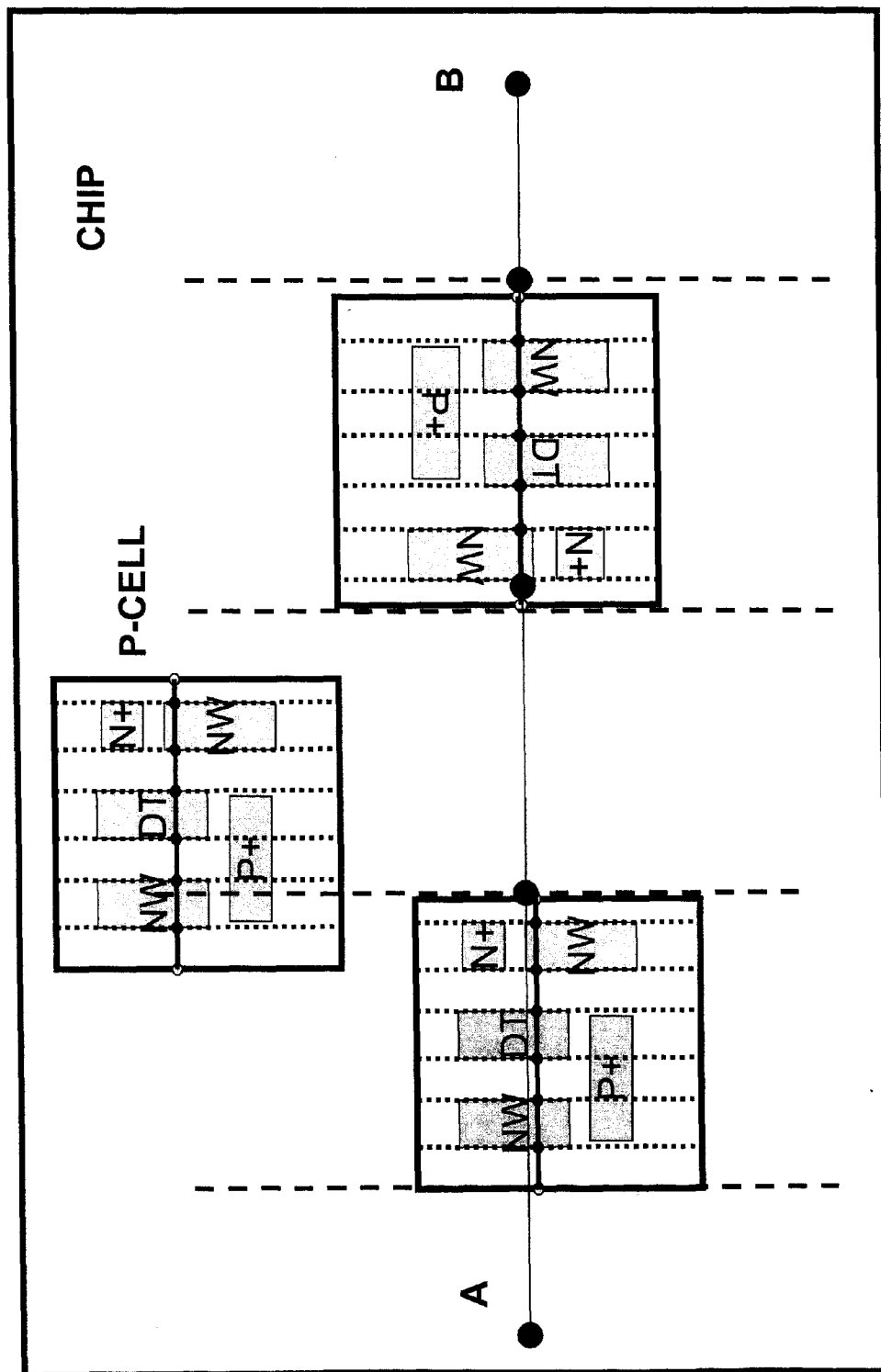


Figure 13



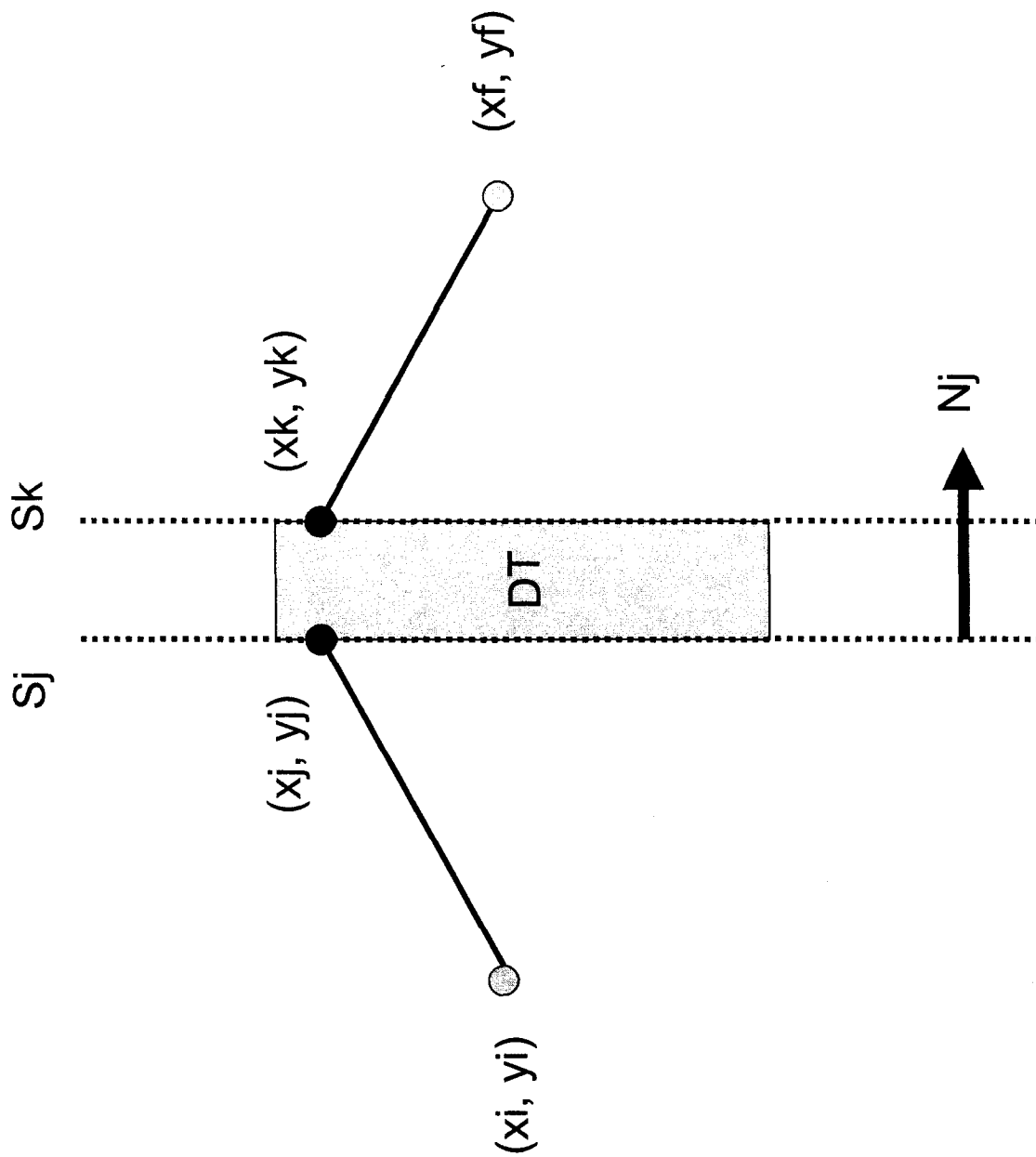
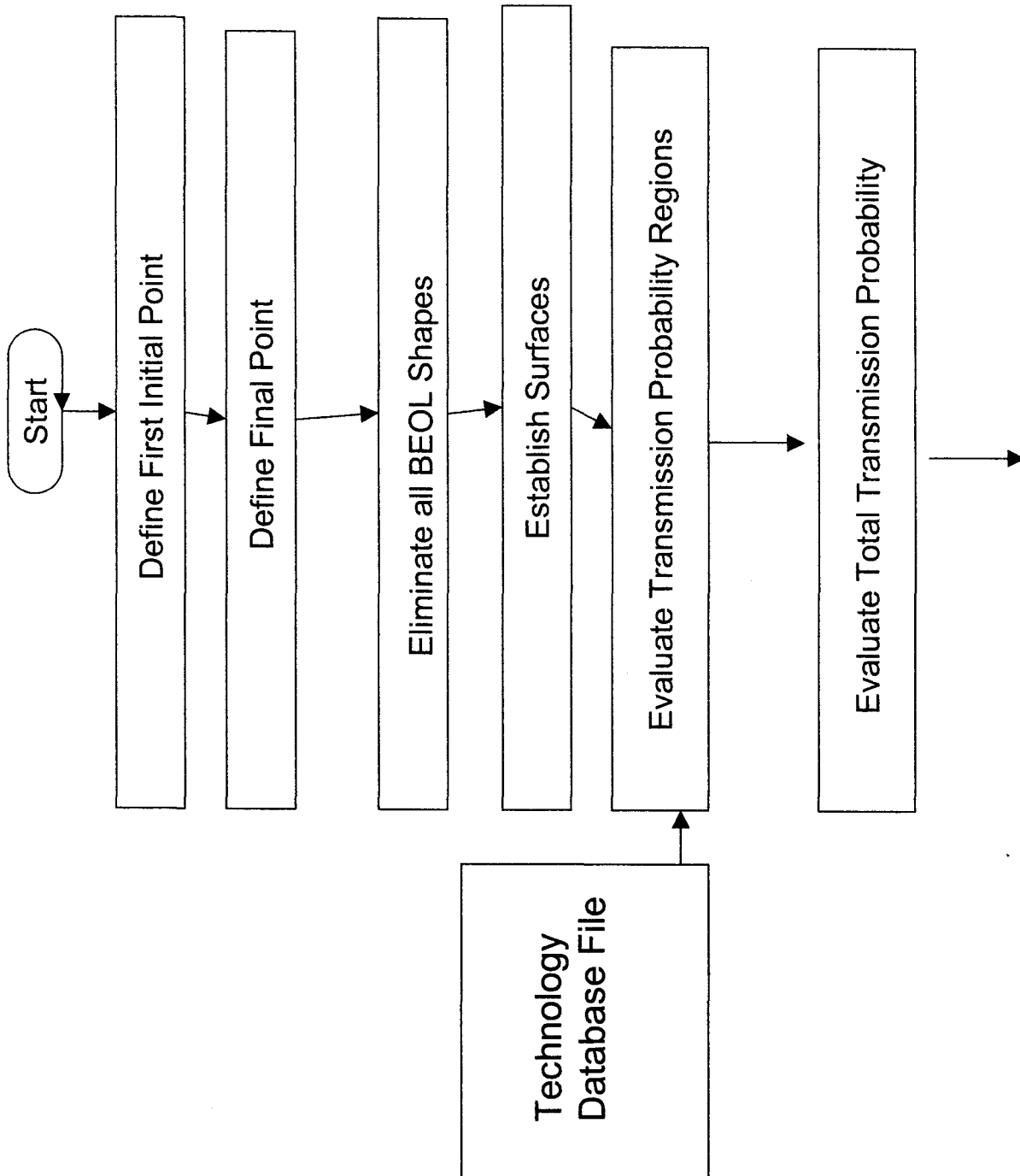
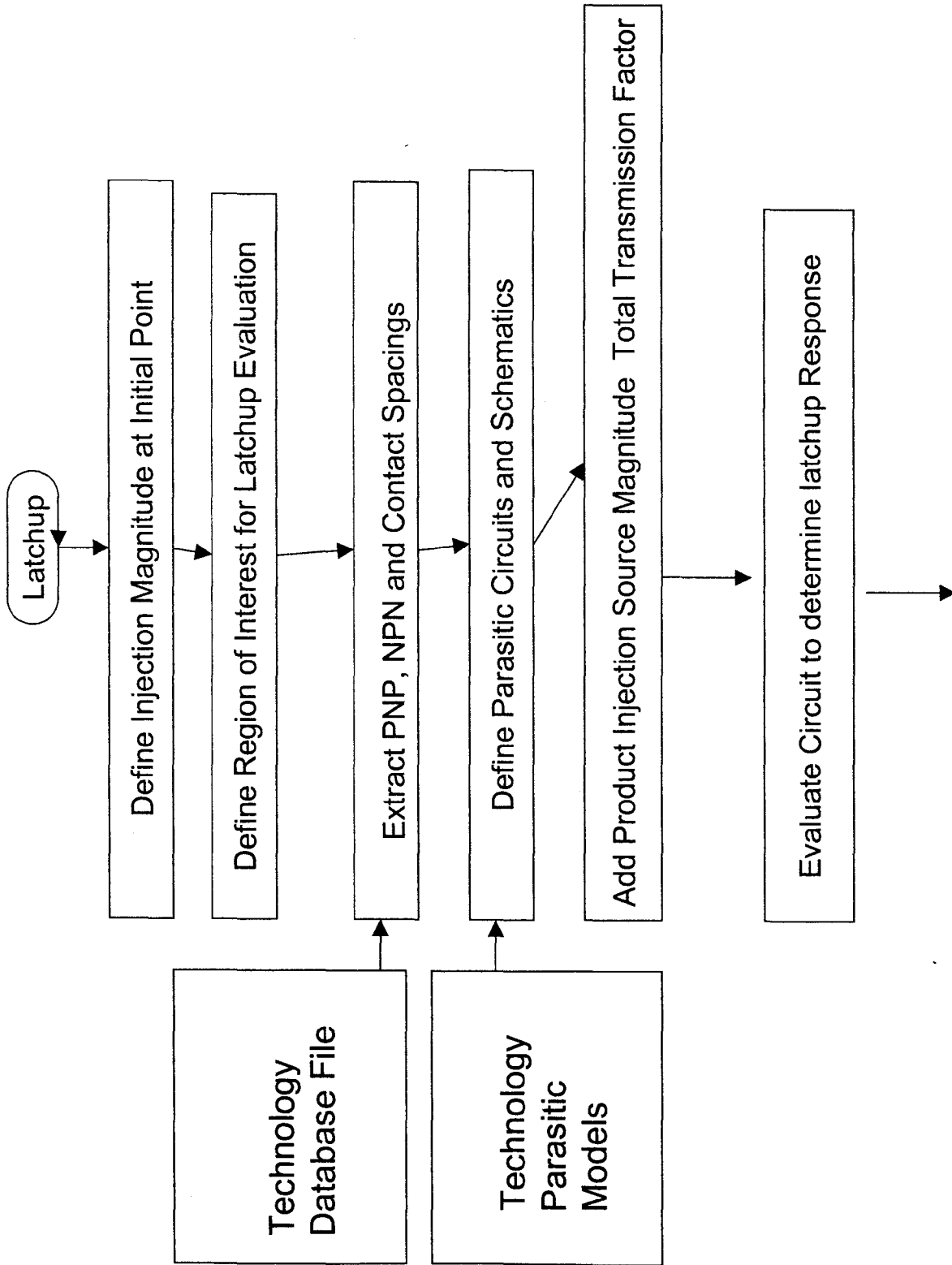


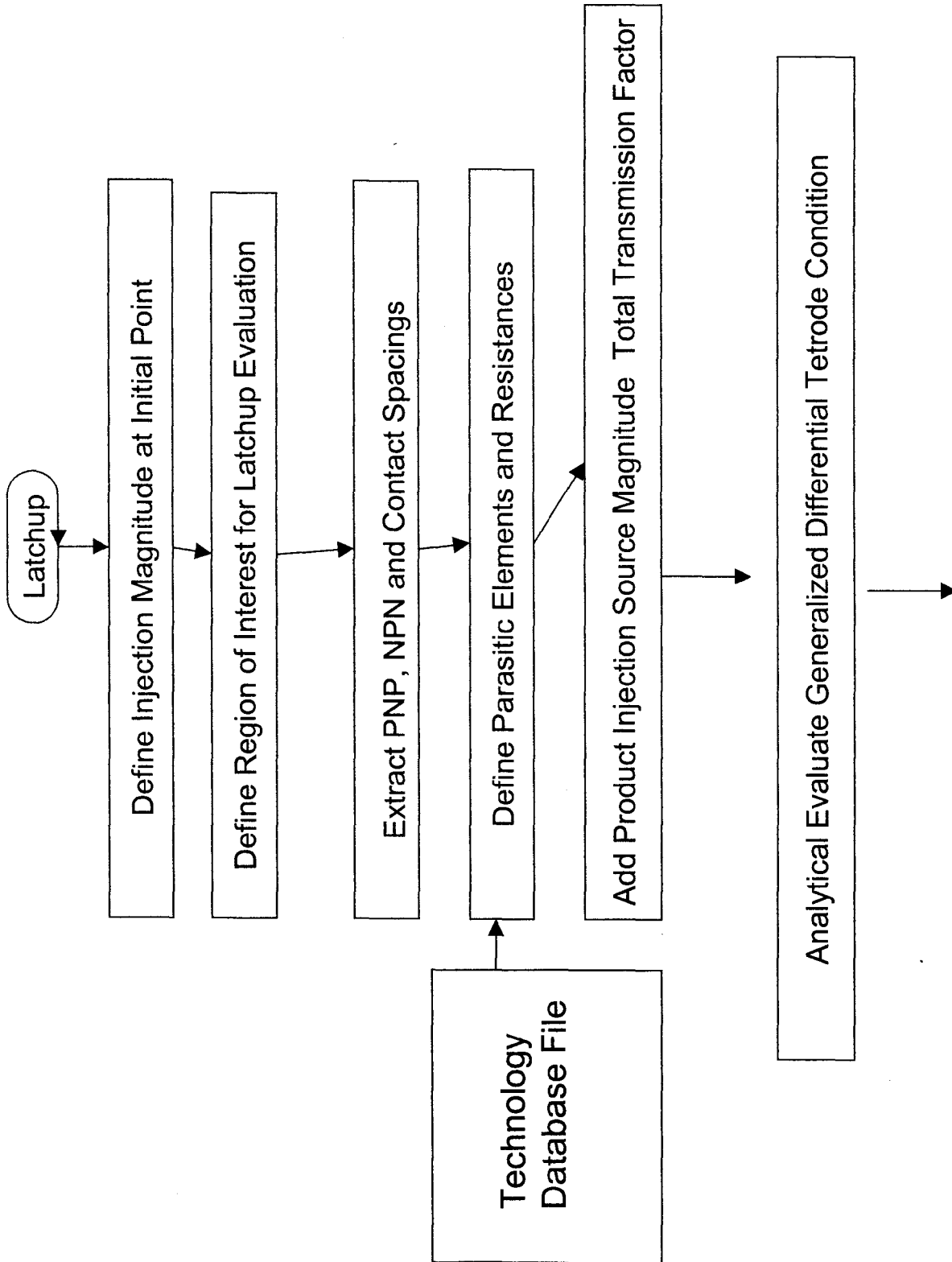
Figure 14



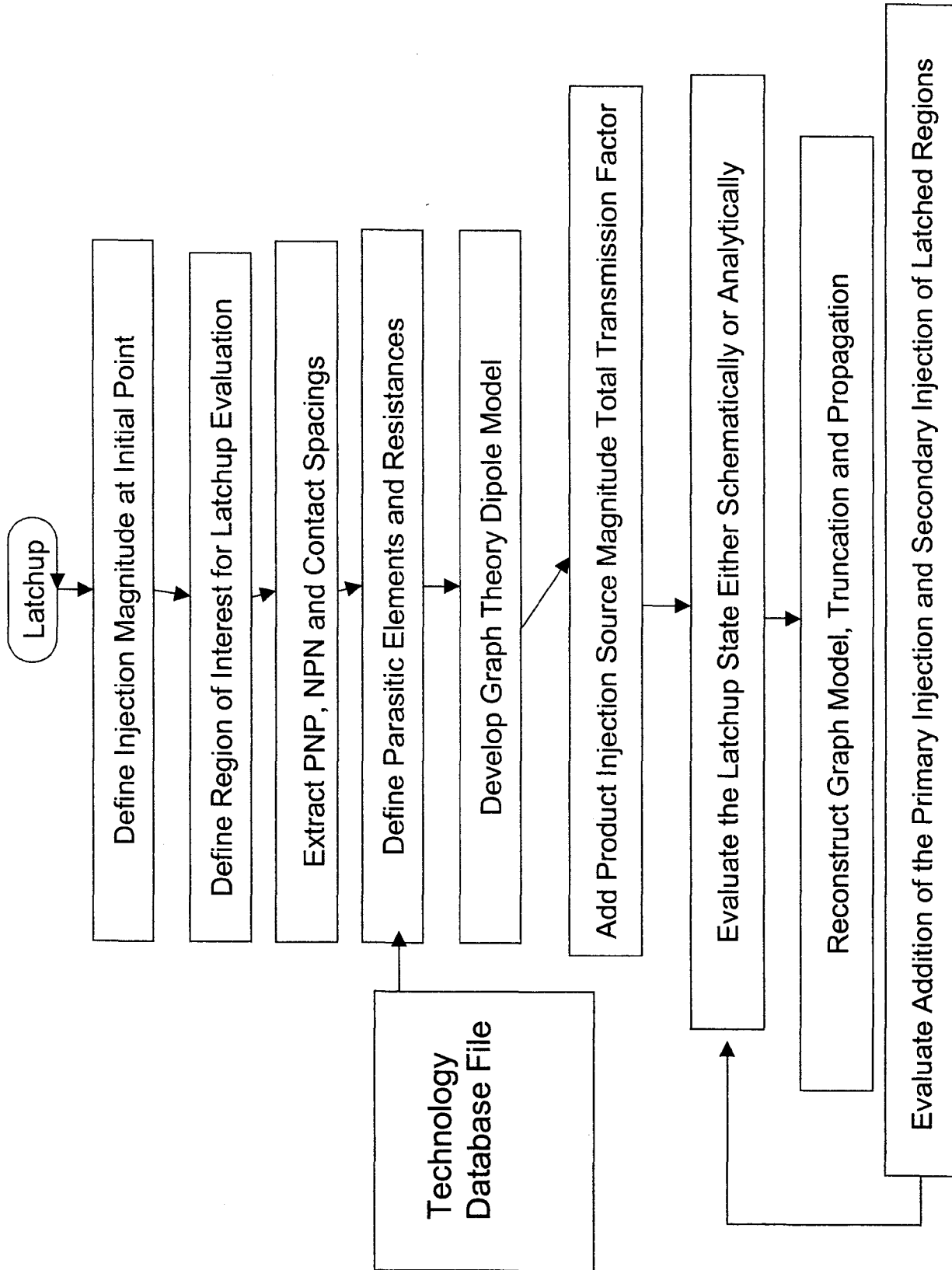
**Figure 15a. Evaluation of the Transmission Probability**



**Figure 15 (b) Evaluation of Latchup using Parasitic Circuit Models**



**Figure 15 (c) Evaluation of Latchup Using Analytical Model**



**Figure 15 (d) Evaluation of Latchup Using Analytical Model**

